

Location: Sophia-Antipolis, France
Employment type: Experienced Professional
Contract type: Permanent position

Ref: HW_SIPV_ENG

SENIOR SOC/IP VERIFICATION

The Automotive industry is living a revolution. Electrification, autonomous driving, diverse mobility, connectivity are trends that are drastically changing the industry's rules. Among all decisive topics revolutionizing cars in the next future, Silicon Mobility is committed to support the rapid advent of electric and hybrid cars.

Silicon Mobility, an Intel company, is a technology leader for cleaner, safer and smarter mobility. The company designs, develops and sells flexible, real-time, safe and open semiconductor solutions named FPCU (Field Programmable Control Unit) for the automotive industry used to increase energy efficiency and reduce pollutant emissions while keeping passengers safe.

The Company is opening an **"Senior SoC/IP Verification"** position in its main Research and Development center ideally located in the Sophia-Antipolis Technology Park on the French Riviera.

You are a brilliant and passionate SoC/IP Verification for System on Chip for Automotive Applications? You want to support the development of disruptive products accelerating the car powertrain electrification? At Silicon Mobility, we like to light up our employees' potential. Are you up for the challenge? Contact us and send your resume and cover letter to recruitment@silicon-mobility.com

ROLE & MISSIONS

As part of the FPCU IC front-end Engineering team, you will work on front-end design activities.

Primary responsibilities of the position include:

- IP/SoC top level test plan definition and writing.
- IP/SoC top level test plan implementation.
- Test execution, debug and coverage.
- SoC top level verification.
- Verification methodology definition.

The position requires pro-active involvement with the whole SoC development team.

REQUIRED SKILLS AND EXPERIENCE

EDUCATION:

- Good university degree in microelectronics engineering.

TECHNICAL SKILLS & EXPERIENCE:

- A minimum of 10 years of experience in Verification with SystemVerilog and UVM.
- Experience in complex IP and SoC verification.
- Experience in Verilog/VHDL RTL.



- Experience in oriented object programming and C/C++ language.
- Good knowledge of SoC architecture and ARM Amba AXI/AHB/APB bus protocol.
- Good knowledge in scripts writing (Makefile, Shell, Perl,...).
- Knowledge of Formal proof is a plus.
- Knowledge of Siemens/Synopsys EDA tools is highly recommended.
- Experience in functional safety ISO26262 will be appreciated.

LANGUAGE SKILLS:

- Fluent in English

BEHAVIORAL SKILLS:

- Be self-motivated, pro-active, flexible and capable of accepting new challenges.
- Be autonomous, organized, rigorous, and have a high sense of priorities.
- Demonstrate strong communication and team working skills.

