



Internship Description

Control Algorithm for DCDC Converter Application (SM-STC 006 / 2025)



## What we offer

	SILICON MOBILITY SAS (registration number 815 085 659 000 RCS Grasse)
Company	The Automotive industry is living a revolution. Electrification, autonomous driving, diverse mobility, and connectivity are trends that are changing the industry's rules. Among all decisive topics revolutionizing cars in the next future, Silicon Mobility is committed to supporting the rapid advent of electric and hybrid cars.
	Silicon Mobility is a technology leader for cleaner, safer, and smarter mobility. The company designs, develops and sells flexible, real-time, safe, and open semiconductor solutions for the automotive industry used to increase energy efficiency and reduce pollutant emissions while keeping passengers safe.
	We are looking for a motivated candidate to join our company in Sophia-Antipolis on the French Riviera.
	Please contact us: recruitment@silicon-mobility.com
Offer ref.	SM-STC 006-2025
Subject – Offer title	Control Algorithm for DCDC Converter Application
Duration	5-6 months— between February/March/April and September 2025
Work hours	35 hours per week, job location at Silicon Mobility office
Education	Last year of Master (BAC+5 or equivalent)
	As part of the R&D System and Software team, the intern will participate in the development of the algorithm for a proposed DCDC control application.  The intern will study the proposed DCDC topology for EV applications.  The intern will contribute to the integration and configuration of the proposed algorithm from specification down to verification using Model In the Loop tests environment, covering the AGILE development flow with the project team. The Silicon Mobility solution is based on the OLEA® FPCU System-on-Chip dedicated to automotive applications.  The purpose of this internship is to analyse, define, develop, configure, and test the solution.
Content/ mission	<ol> <li>During the internship period, several tasks will be addressed:         <ol> <li>Requirements analysis</li> <li>As part of this task, the intern will have to study the proposed DCDC topology, the DCDC control algorithms, the architecture of OLEA® FPCU, and the development tools used. A particular attention will be paid to the characterization of key performance parameters.</li> </ol> </li> <li>System and Software specification         <ol> <li>Based on the previous analysis, the intern will collaborate with other R&amp;D team members in the writing of a detailed specification of the solution in collaboration. This specification shall cover the following aspects:</li></ol></li></ol>



Profile required	We are looking for a candidate with good knowledge of electrical engineering, power electronics, and control algorithms.  Good skills in electrotechnics, DCDC topologies, Matlab Simulink/ Simscape, and/or DSPACE HIL Bench.  The candidate shall be autonomous, rigorous with a strong team spirit.  English speaking is required.
Expected Skills/knowledge	<ul> <li>Matlab/Simulink and SimScape Toolbox</li> <li>Power electronics transistors</li> <li>Functional Safety</li> <li>Advanced algorithm control for power converters</li> <li>General knowledge of microcontroller development</li> <li>Critical real-time embedded software on ARM processor</li> <li>General knowledge of microcontroller development</li> <li>Requirement analysis and specification writing</li> <li>Quality management skills</li> <li>Notions of planning and project management</li> <li>Quality management skills</li> </ul>
Remuneration	1400€/month + Lunch tickets + Public transport